

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant :	Isamu Kobori, et al.	Art Unit :	Unknown
Serial No. :	New Divisional Application	Examiner :	Unknown
Filed :	November 17, 2003	Confirmation No.:	Unknown
Title :	METHOD OF MANUFACTURING A SEMICONDUCTOR METHOD OF MANUFACTURING A THIN-FILM TRANSISTOR AND THIN-FILM TRANSISTOR		

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

Prior to examination, please amend the application as indicated on the following pages.

Amendments to the Specification begin at page 2 of this paper.

Amendments to the Claims are reflected in the listing of claims which begins on page 6 of this paper.

Amendments to the Drawings begin on page 11 of this paper and include both a replacement sheet and an annotated sheet showing changes.

Remarks/Arguments begin on page 12 of this paper.

An **Appendix** including amended drawing figures is attached following page 12 of this paper.